

Application No. 10/784,755  
Response to Restriction Requirement dated March 14, 2006  
Reply to Office Action mailed February 14, 2006

### **CLAIM AMENDMENTS**

Please replace the pending claims with the following listing of claims:

1-8. (Withdrawn)

9. (Original) In a system that delivers a main pulse to a load, a circuit for shortening a rise time of the main pulse delivered to the load, the circuit comprising:

a main pulse transformer coupled to a load, the main pulse transformer configured to deliver a main pulse to the load, wherein a rise time of the main pulse is related to a capacitance associated with the load;

an auxiliary pulse transformer coupled to the load, wherein the auxiliary pulse transformer delivers an auxiliary pulse to charge the capacitance associated with the load;  
and

a diode connected between the auxiliary pulse transformer and the main transformer to isolate the auxiliary pulse transformer from the main pulse.

10. (Original) A circuit as in claim 9, further comprising a main pulse generator that generates the main pulse delivered to the load through the main pulse transformer.

11. (Original) A circuit as in claim 9, further comprising an auxiliary pulse generator that generates the auxiliary pulse delivered through the auxiliary pulse transformer.

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12. (Original) A circuit as in claim 9, the main pulse having a duration that is longer than the duration of the auxiliary pulse.

13. (Original) A circuit as in claim 12, the auxiliary pulse being delivered through the auxiliary pulse transformer before the main pulse is delivered to the load through the main pulse transformer.

14. (Original) A circuit as in claim 12, the auxiliary pulse being delivered through the auxiliary pulse transformer such that the auxiliary pulse overlaps a first portion of the main pulse.

15. (Original) A circuit as in claim 12, the auxiliary pulse being delivered through the auxiliary pulse transformer to charge the capacitance to increase a rise time of the main pulse across the load.

16. (Original) A circuit as in claim 15, the auxiliary pulse being configured to reduce ringing of the main pulse on the load.

17. (Original) A circuit as in claim 15, further comprising means for adjusting the auxiliary pulse to minimize a rise time of the main pulse.

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18. (Original) A method for decreasing a rise time of a main pulse delivered to a load through a main pulse transformer, the method comprising:

applying an auxiliary pulse to a load through an auxiliary pulse transformer such that the auxiliary pulse charges a stray capacitance associated with the load;

applying a main pulse to the load through a main pulse transformer, the main pulse having a rise time that is related to the stray capacitance, wherein the second pulse reduces the rise time; and

terminating the auxiliary pulse after the stray capacitance associated with the load is charged.

19. (Original) A method as defined in claim 18, wherein applying an auxiliary pulse further comprises applying the auxiliary pulse such that the auxiliary pulse overlaps with a beginning portion of the main pulse.

20. (Original) A method as defined in claim 18, wherein applying an auxiliary pulse further comprises applying the auxiliary pulse such that the auxiliary pulse does not overlap with the main pulse.

21. (Original) A method as defined in claim 18, wherein applying an auxiliary pulse further comprises isolating the auxiliary pulse transformer from the main pulse.

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22. (Original) A method as defined in claim 18, further comprising adjusting at least one of a timing, an amplitude, and a duration of the auxiliary pulse to reduce a rise time of the main pulse.

23. (Original) A method as defined in claim 18, further comprising adjusting at least one of a timing, an amplitude, and a duration of the auxiliary pulse to reduce a ringing of the main pulse on the load.